

United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspio.gov

ATTORNEY DOCKET NO. CONFIRMATION NO. APPLICATION NO. FILING DATE FIRST NAMED INVENTOR 51876P453 9280 12/17/2003 10/739,434 Soon-Yong Kweon EXAMINER 8791 7590 08/08/2005 BLAKELY SOKOLOFF TAYLOR & ZAFMAN AHMED, SHAMIM 12400 WILSHIRE BOULEVARD PAPER NUMBER ART UNIT SEVENTH FLOOR LOS ANGELES, CA 90025-1030 1765

DATE MAILED: 08/08/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

	•		\mathcal{M}
Office Action Summary	Application No.	Applicant(s)	
	10/739,434	KWEON ET AL.	
	Examiner	Art Unit	
	Shamim Ahmed	1765	
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply			
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).			
Status			
1) Responsive to communication(s) filed on 17 De	ecember 2003.		
2a) ☐ This action is FINAL . 2b) ☑ This	action is non-final.		
3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is			
closed in accordance with the practice under E	x parte Quayle, 1935 C.D. 11, 45	i3 O.G. 213.	
Disposition of Claims			
4) Claim(s) 1-15 is/are pending in the application.			
4a) Of the above claim(s) is/are withdrawn from consideration. 5) Claim(s) is/are allowed.			
6)⊠ Claim(s) <u>1-3 and 6-15</u> is/are rejected.			
7)⊠ Claim(s) <u>4 and 5</u> is/are objected to.			
8) Claim(s) are subject to restriction and/or	r election requirement.		
Application Papers			
9)☐ The specification is objected to by the Examine	r.		
10) \boxtimes The drawing(s) filed on <u>17 December 2003</u> is/are: a) \boxtimes accepted or b) \square objected to by the Examiner.			
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).			
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d). 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.			
Trib oath of declaration is objected to by the Ex	animer. Note the attached Office	ACION OF IOIN P	10-152.
Priority under 35 U.S.C. § 119			
 12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority documents 2. Certified copies of the priority documents 3. Copies of the certified copies of the priori application from the International Bureau * See the attached detailed Office action for a list 	s have been received. s have been received in Applicati ity documents have been receive I (PCT Rule 17.2(a)).	on No ed in this National	l Stage
Attachment(s)			
1) Notice of References Cited (PTO-892)	4) Interview Summary		
2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)	Paper No(s)/Mail Da 5) Notice of Informal P	ate	O-152)
Paper No(s)/Mail Date	6) Other:	and it is a production of the	

Me

Art Unit: 1765

DETAILED ACTION

Claim Rejections - 35 USC § 103

- 1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 2. This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).
- 3. Claims 1-3, 6-15 are rejected under 35 U.S.C. 103(a) as being unpatentable over Applicant's admitted prior art (AAPA) in view of Takeno et al (5,178,725).

Applicant's admitted prior art (AAPA) discloses a process of fabricating a ferroelectric random access memory device including the steps of:

Forming a first inter-layer insulating layer on a substrate, which insulating layer is etched to form a storage node contact hole exposing a partial portion of the substrate (page 2, lines 12-26);

Art Unit: 1765

> Forming plug within the contact hole and barrier metal layer (page 3, lines 13-22);

- ➤ Forming an adhesion layer of alumina on the storage node contact and the first inter layer insulating layer, selectively etching a predetermined portion of the adhesion layer to expose a surface of the barrier metal layer formed on the plug (page 4, lines 5-17);
- Forming ferroelectric capacitor connected to the plug (page 4, lines 18-27).

AAPA fails to teach inducing the predetermined portion of the adhesion layer to form crack in the predetermined portion, which is then removed.

However, in a method of electronic device fabrication, Takeno et al disclose a process of irradiating a ceramic substrate such as alumina in order to form affected areas having cracks (see, abstract) and then removing the affected areas by utilizing various procedures such as etchant solution including aqueous solution of acids or potassium or sodium hydroxide (col.3, lines 61-68).

Therefore, it would have been obvious to one of ordinary skilled in the art at the time of claimed invention to combine Takeno et al's teaching into AAPA's process for efficiently removing the adhesion layer as taught by Takeno et al.

Allowable Subject Matter

4. Claims 4-5 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Art Unit: 1765

5. The following is a statement of reasons for the indication of allowable subject matter: the prior art does not teach or suggest a process including the step of forming the crack in the adhesion layer by performing a rapid thermal annealing.

Conclusion -

6. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Song (6,191,049) teaches RTA is conducted to cure the defect or damages caused by ion injection; Park (6,867,094) teaches RTA is performed on dielectric layer of alumina; and Yang (6,238,934) teaches RTA is performed for nucleation in the ferroelectric layer. Lee (6,603,169) teaches high temperature thermal treatment is typically required to enhance dielectric characteristics.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Shamim Ahmed whose telephone number is (571) 272-1457. The examiner can normally be reached on M-Thu (7:00-5:30) Every Friday Off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nadine G. Norton can be reached on (571) 272-1465. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Art Unit: 1765

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Shamim Ahmed Primary Examiner Art Unit 1765

SA August 4, 2005